Eric Qian

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Summary

- ❏ Enthusiastic tinkerer who's passionate about contributing out-of-box thinking to disruptive projects and applying laboratory education into real-world applications.

Education

California Polytechnic State University - San Luis Obispo
Expected Graduation: June 2023 — San Luis Obispo, CA

- ❏ Bachelor of Science in Computer Engineering
- ❏ Cumulative GPA: 3.65 | Major GPA: 3.94
- ❏ Relevant Coursework:

Electric Circuit Analysis	Circuit Manufacturing	C Data Structures
RISC-V Assembly and Design	Technical Writing	Python Data Structures
FPGA Digital Design	JavaScript	Bash Scripting

Work Experiences And Projects

SoC Design Verification Engineer, Digital IP/Chip Design - [Samsung Semiconductor R&D]
June. 2021 to Sept 2021 — Remote, Remote

- ❏ Created and maintained randomized UVM test benches written in C++ & SystemVerilog to increase functional, toggle, and code coverage.
- ❏ Debugged top-level and block-level test benches failures and worked with system architects to resolve design issues.
- ❏ Created debug GUIs that accelerate debug flow using tcl via Synopsys API.
- ❏ Developed knowledge of industry-standard tools and debug methodologies.

IoT Full Stack Developer - [Trackversal]
Project Description: Portable Multi-Technology Device For Global Asset Tracking
Nov. 2019 to Present — Cupertino, CA

- ❏ IoT ARM Firmware development with Nordic Semiconductor nRF chips via SEGGER Embedded Studio.
- ❏ Implemented serial communication between subsystems.
- ❏ Developed backend API written in NodeJS with Express and MongoDB.
- ❏ Worked on frontend stack for mobile devices built with React Native.
- ❏ Completed hardware integration by sourcing proper components and optimized IoT power consumption by more than 75%.
- ❏ Evaluated technical feasibility and market demand.

Electrical Engineer - [Cal Poly CubeSat Laboratory]
Project Description: Battery Management Systems For Spacecrafts
September. 2020 to Present — San Luis Obispo, CA

- ❏ Project Subsystem Lead For Modular Satellite Backplane.
- ❏ Launched trade study investigation for power management IC selection.
- ❏ Performed battery subsystem risk analysis to maximize safety for spacecraft applications.

Instructional Student Assistant - [Cal Poly, Electrical Engineering & Computer Science]
Apr. 2020 to Present — San Luis Obispo, CA

- ❏ Assisted students in learning by overcoming dynamic technical challenges during laboratory work and providing debugging assistance. This includes the use of oscilloscopes, DMMs, and other laboratory equipment, in addition to verifying circuit designs.
- ❏ Rapid on-demand troubleshooting of algorithms and code for Python and C programs.
- ❏ Provided students with technical support for adjustment to a digital-only learning environment and increased class performance quarter-to-quarter.

Experiences

Hardware Related

- ❏ Eagle, Altium Designer
- ❏ LTSpice
- ❏ Circuit Design
- ❏ Verilog
- ❏ Cura & Slic3r
- ❏ Circuit Verification, Assembly, and Debugging
- ❏ SEGGER ARM Development
- ❏ Verdi

Web Related Technologies

- ❏ JavaScript
- ❏ HTML & CSS
- ❏ PHP
- ❏ Firebase
- ❏ JQuery

Standalone Technologies

- ❏ Bash, Apt, Pacman
- ❏ Python
- ❏ Java
- ❏ C
- ❏ .NET (C#)
- ❏ Linux/Embedded Linux

Workflow Related Technologies

- ❏ Microsoft Word/Excel
- ❏ Git & GitHub
- ❏ Travis CI & Unit testing
- ❏ VSCode Live Share
- ❏ ~110 WPM

Organizations

- ❏ BananiumLabs Incorporated
- ❏ Cal Poly CubeSat Laboratory
- ❏ IEEE-HKN Honor Society
- ❏ GLAARC VEC

Credentials

- ❏ Amateur Radio - General Class License

Languages

- ❏ English (Fluent/Native Speaker)
- ❏ Mandarin (Professional Proficiency)

Misc.

- ❏ Numerous Hackathon Submissions (Devpost.com/EnumC)